

Figura 2-7 - Taxonomia dos diagramas SysML

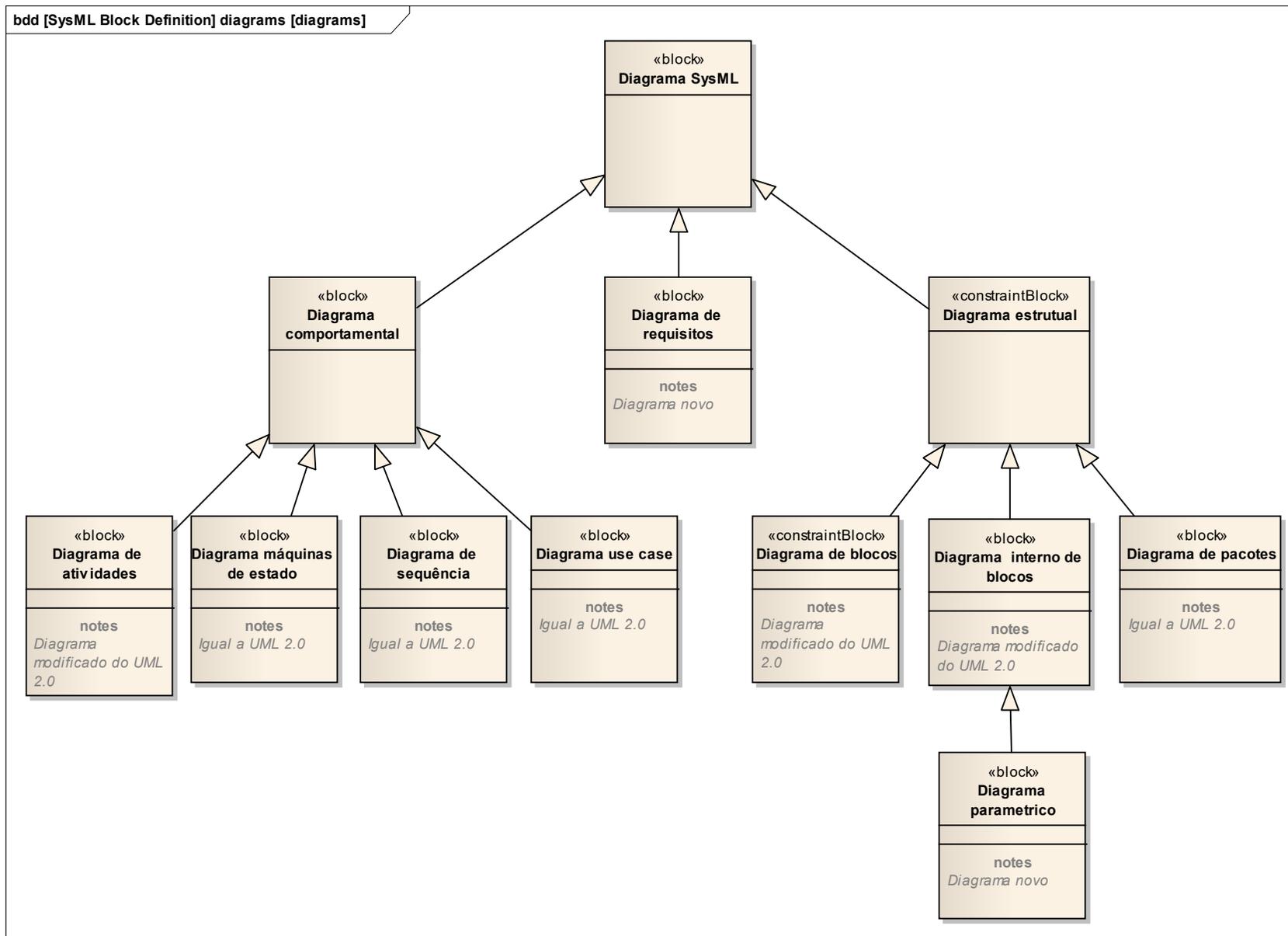




Figura 5-2 – Processo de Projetar do produto

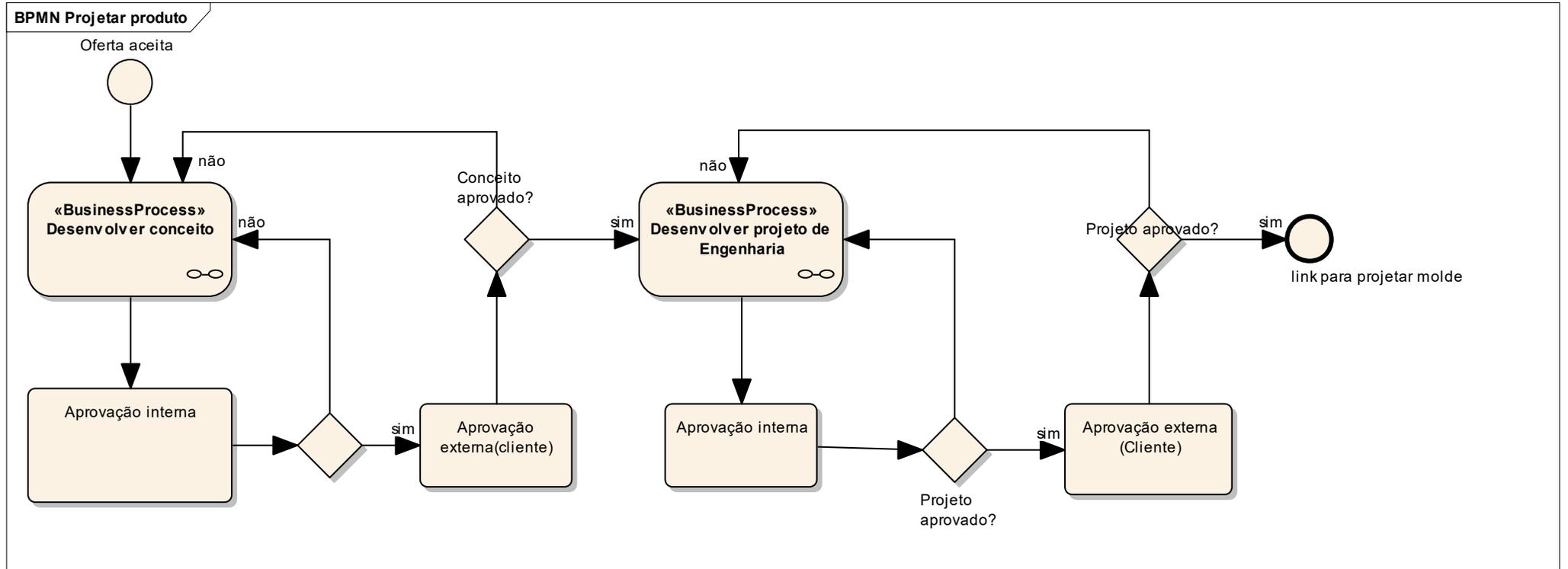


Figura 5-13 – Processo de determinação de indicadores de desempenho do processo

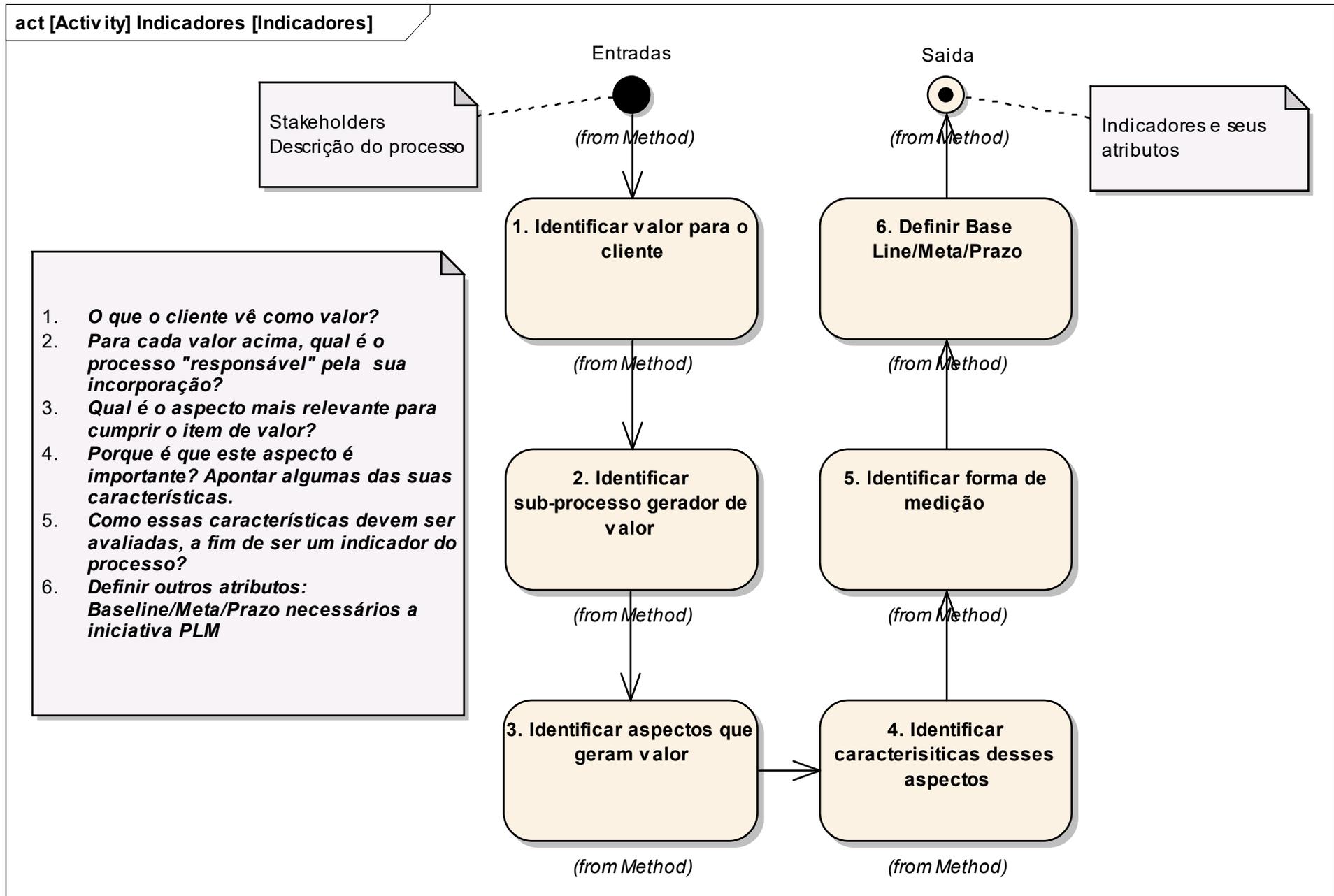


Figura 5-21 – Diagrama de Casos de uso

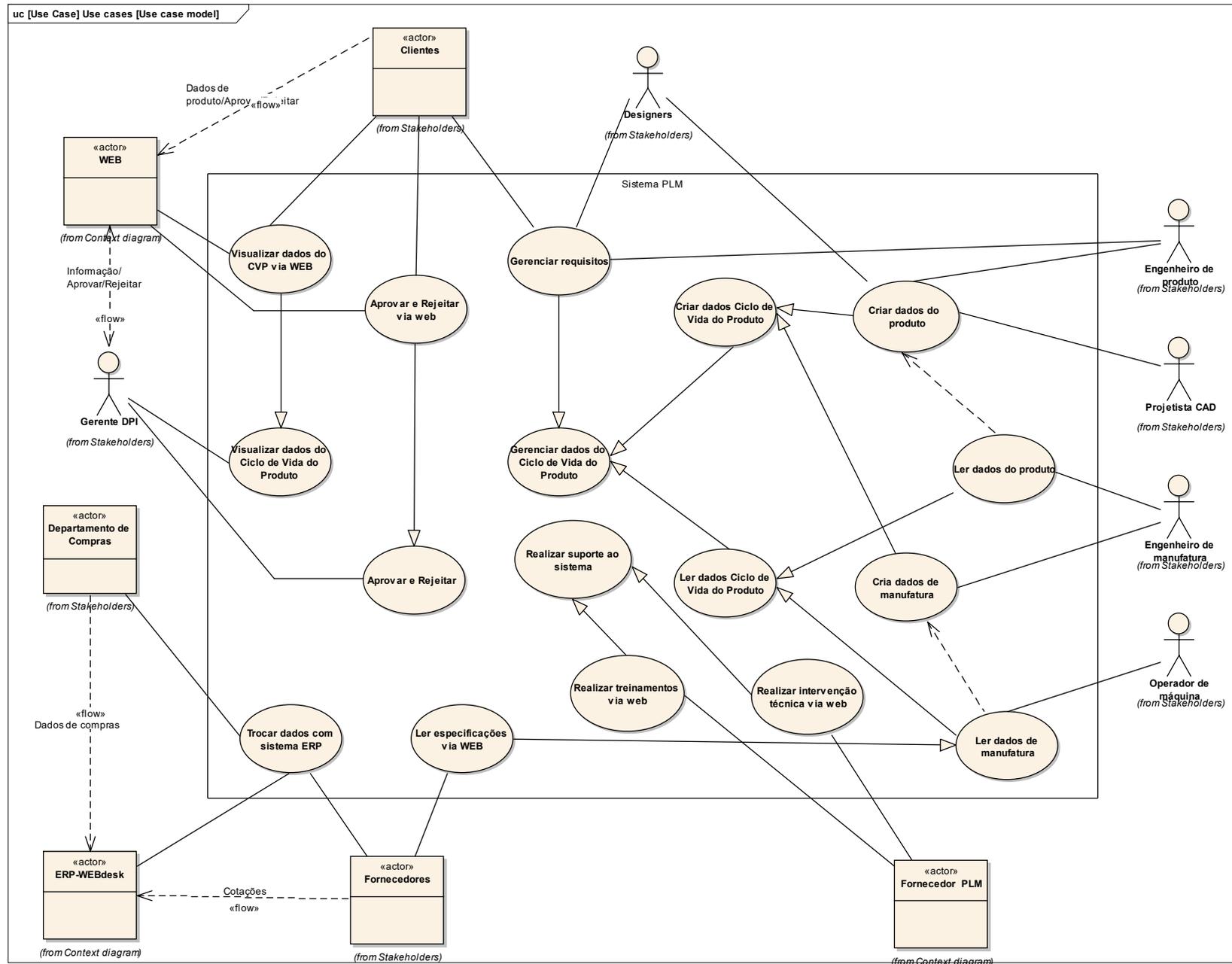


Figura 5-22 – Diagrama de blocos representando a estrutura do sistema

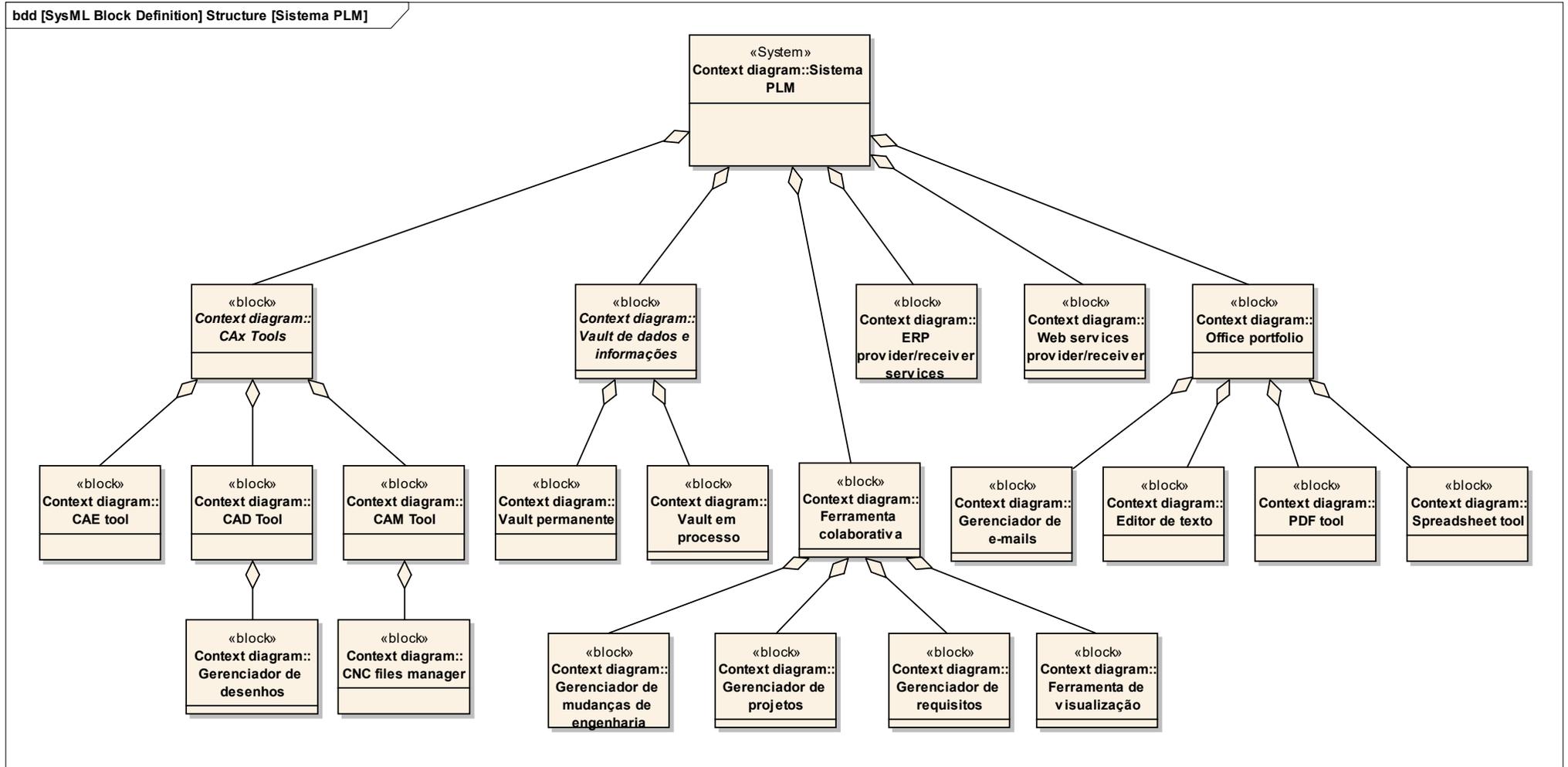


Figura 5-23 – Requisitos determinados para PLM

